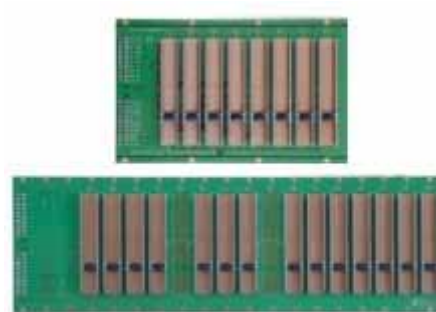


PXI

Key Features:

- Conform to PXI R2.1
- Support Hot Swap feature of PICMG 2.1 R2.0
- Daisy-chained Local Bus
- Trigger Bus
- System reference Clock
- Star Trigger slot
- VI/O are user selectable to a +5V or +3.3V
- All signal lines characteristic impedance are set to 65Ω
- FR4 material PCB
- 2 m/m HM connector



U	Slots	MHz	Bit	Connector	System	P/N
3	8	33/66	32/64	P1S, P2S	None	CHTTL-PXI-3U8SBKP-A2